



Class	Subclass

ISSUE CLASSIFICATION

PATENT NUMBER

U.S. **UTILITY** Patent Application

<p>O.I.P.E.</p> <p>12/11/11</p> <p>SCANNED <u>11/11</u> Q.A. <u>11/11</u></p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/667559	CONT/PRIOR F	CLASS 257	SUBCLASS 500+	ART UNIT 281 2814	EXAMINER FARAHANI CHEN HUI
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APPLICANTS

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Power MOS transistor having trench gate

Table

PTO-2040
12/99

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		NOTICE OF ALLOWANCE MAILED	
			_____ (Date)	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner)			
			_____ (Date)	
<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)		_____ (Date)	

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